DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled SQUARING CELLS AND MULTIPLIERS USING SUMMED EXPONENTIALS, the specification of which:

[X] is attached hereto.				
Ī] was filed on	as Application Serial No			
		n			
Ī	with amendments th	rough	(if applicable)		
		viewed and understand the cont ns, as amended by any amendm			
	-	lisclose information, which is most Federal Regulations, Sec. 1.56			
Sec. 119(a)-(d) and have also id	of any foreign application(s lentified below any foreign	rity benefits under Title 35, Units) for patent or inventor's certificapplication for patent or inventoration on which priority is claim	cate listed below or's certificate		
Prior Foreign Application(s)		P	Priority Claimed		
(Number)	(Country)	(Day/Month/Year Filed)	[] [] Yes No		
	hereby claim the benefit uses provisional application lessons	nder Title 35, United States Cooisted below:	le, Sec. 119(e) of		
Provisional Application No.		<u>Fil</u>	Filing Date		

I hereby claim the benefit under Title 35, United States Code, Sec. 120 of any United States application(s), or Sec. 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code, Sec. 112. I

acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Sec. 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Serial No. 09/473,309	December 28, 1999	US Patent No. 6,437,6306 BI
Serial No. 09/569,544	May 12, 2000	US Patent No. 6,429,720 B1
Serial No. 10/170,067	June 11, 2002	US Patent No. 6,525,601 B2
Serial No. 10/192,115	July 9, 2002	pending
Serial No. 10/319,368	December 12, 2002	pending
(App. Serial No.)	(Filing Date)	(Status -patented, pending, etc.)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

)			
Full name of sole inventor:	Barrie Gilbert		
Inventor's signature:		(Date)	, 2004
Residence:	Portland, Oregon		
Citizenship:	United Kingdom		
Post Office address:	c/o Analog Devices, Inc. 1100 N.W. Compton Drive. Suite 301		

Beaverton, OR 97006-1994